# **Improving Your G8RTOS** EEL 4745C: Microprocessor Applications II Fall 2022

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Lecture 5



#### Overview

- Discussions on: improving your G8RTOS implementation in Lab-2
- In Lab-3, you will do the following:
  - Improve semaphores using the blocking and yielding features
  - Add sleeping feature to background threads to free up CPU time as opposed to a delay
  - Integrate periodic threads in conjunction with multiple background threads, and
  - Implement IPC: Inter-Process Communication using FIFOs
- You have to demonstrate:
  - Periodic threads are working alongside background threads
  - Consistent IPC is happening based on FIFO principles
  - Joystick, temperature sensor, light sensors values are manipulated correctly
    - See the lab-3 manual for details



#### Improved Semaphore

#### **Blocked Semaphore**

- Previously, we implemented a simple spinlock semaphore; the continuous spin-locks wasted CPU memory
- We will improve this by adding a blocked flag in TCB structure. If the blocked flag was set, the blocked thread will yield the CPU control to next thread during the SysTick handler.

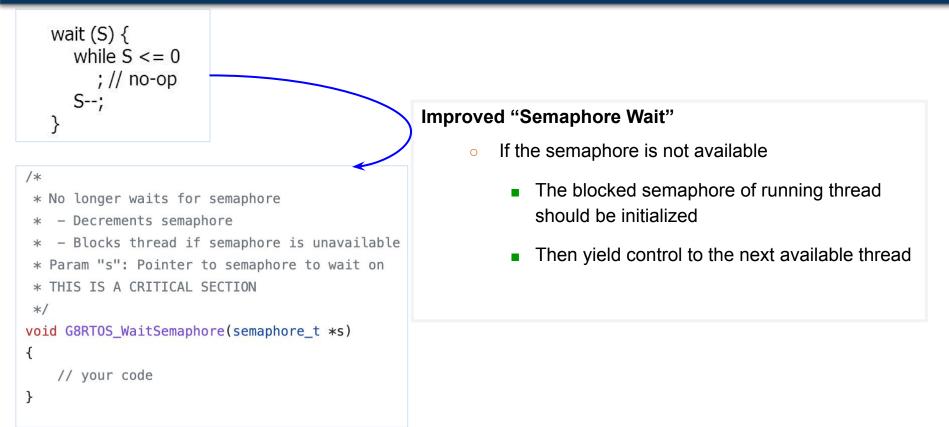
```
typedef struct tcb_t {
    int32_t *stackPointer;
    struct tcb_t *nextTCB;
    struct tcb_t *previousTCB;
    semaphore_t *blocked;
    uint32_t sleepCount;
    bool asleep;
} tcb_t;
```





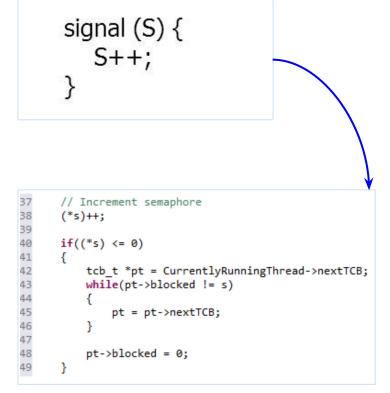


### Improved Semaphore: Wait





#### Improved Semaphore: Signal



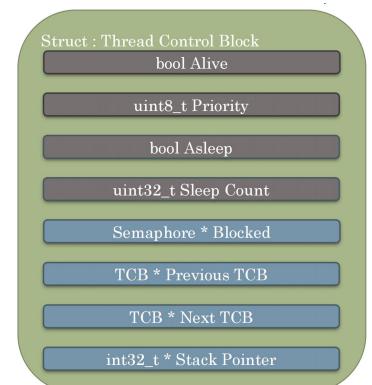
#### Improved "Signal Semaphore"

- If the semaphore is not available
  - Go through the TCB list and unblock the first thread blocked on the same semaphore.\
  - Move that unlocked thread to the next thread to be executed





```
typedef struct tcb_t {
    int32_t *stackPointer;
    struct tcb_t *nextTCB;
    struct tcb_t *previousTCB;
    semaphore_t *blocked;
    uint32_t sleepCount;
    bool asleep;
} tcb_t;
```





# Sleeping

- Active State: Thread is ready to run but waiting for its turn
- You Sleep State: Thread is waiting for a fixed amount of time before it enters the active state again
- Blocked State: Thread is waiting on some external or temporal event
- Blocking and sleeping help to free up the processor to perform other tasks as opposed to just "spinning" (wasting its entire time slice checking if the event condition is met)

```
/*
 * Puts the current thread into a sleep state.
 * param durationMS: Duration of sleep time in ms
 */
void sleep(uint32_t durationMS)
{
    CurrentlyRunningThread->sleepCount = durationMS + SystemTime;
    CurrentlyRunningThread->asleep = 1;
    HWREG(NVIC_INT_CTRL) |= NVIC_INT_CTRL_PEND_SV;
}
```





# Using the Sleep Function

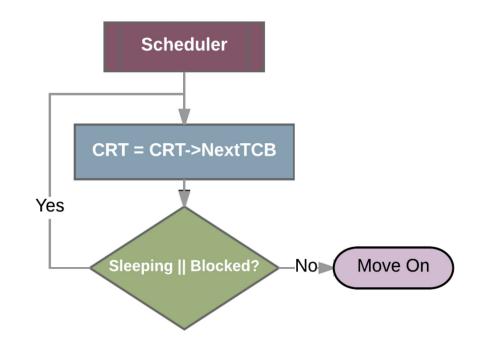
- In the SysTick handler, check every sleeping thread's sleep count
- If the thread's sleep count is equal to the current SystemTime
  - Then that thread is to be wake up
  - Otherwise, it remains sleeping

```
/*
 * Puts the current thread into a sleep state.
 * param durationMS: Duration of sleep time in ms
 */
void sleep(uint32_t durationMS)
{
    CurrentlyRunningThread->sleepCount = durationMS + SystemTime;
    CurrentlyRunningThread->asleep = 1;
    HWREG(NVIC_INT_CTRL) |= NVIC_INT_CTRL_PEND_SV;
}
```



**Note:** It is possible that all threads can be either sleeping or blocked, in which case we enter an infinite loop here.

How do we avoid this in Lab-3?







# Improved SysTick Handler

void SysTick\_Handler()

SystemTime++; tcb\_t \*ptr = CurrentlyRunningThread; ptcb\_t \*Pptr = &Pthread[0]; *Increments system time Gets the current threads (periodic and background)* 

Loop through the periodic threads, and execute them appropriately (if their time is now!)

Loop through the background threads: check sleeping threads and wake them up appropriately (if their time is now!)

// now lets do the context switch
HWREG(NVIC\_INT\_CTRL) |= NVIC\_INT\_CTRL\_PEND\_SV;

Context Switch





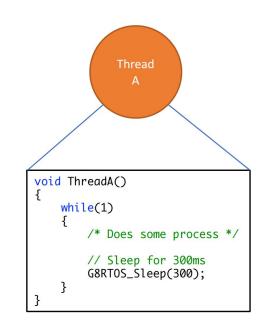
### **Alternate Sleeping Implementation**

- Another way to implement sleeping is to
  - **Remove** the new sleeping thread from the linked list of active threads, and
  - **Insert** it to a doubly linked list of sleeping threads
- This list of sleeping threads will be **sorted** from smallest to highest sleep count
- Once the thread with the lowest sleep count equals the system time, that thread is woken up
- Advantage:
  - Now we only have to check one sleeping thread's sleep count within the SysTick handler as opposed to every initialized thread

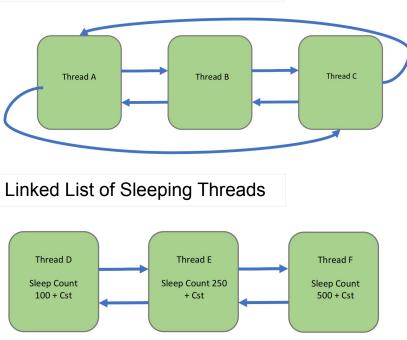
Lab-3 Bonus point: +1





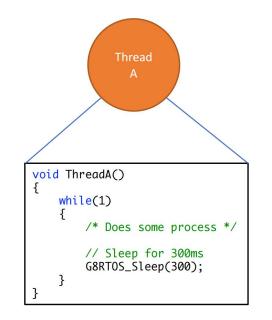


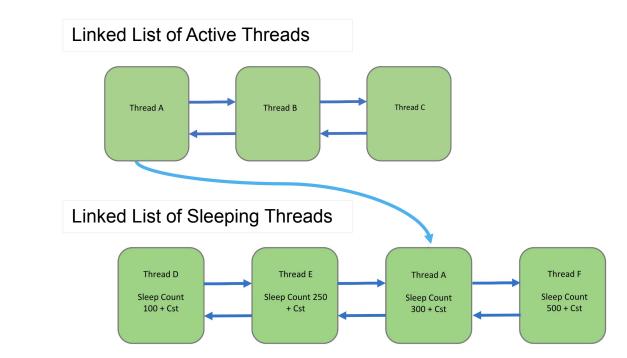
Linked List of Active Threads





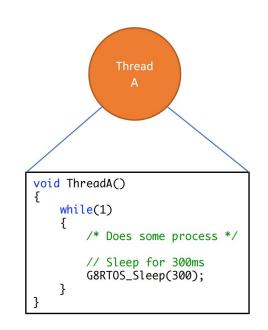












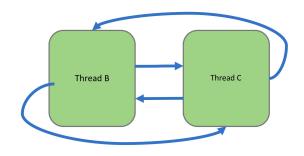
Linked List of Active Threads Thread B Thread C Linked List of Sleeping Threads Thread D Thread E Thread A Thread F Sleep Count 250 **Sleep Count** Sleep Count **Sleep Count** 500 + Cst 100 + Cst + Cst 300 + Cst



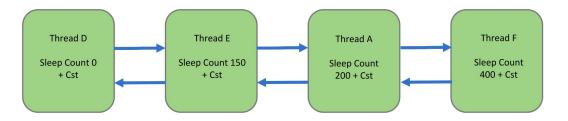


Linked List of Active Threads

Now that the System Time has incremented enough times to equal Thread D's Sleep Count, it is time to add Thread D back into the Active Thread Linked List



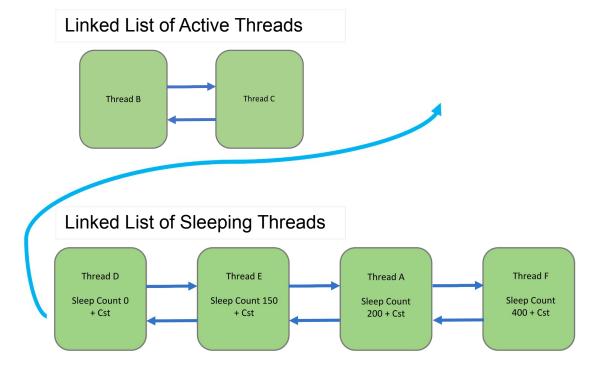
#### Linked List of Sleeping Threads



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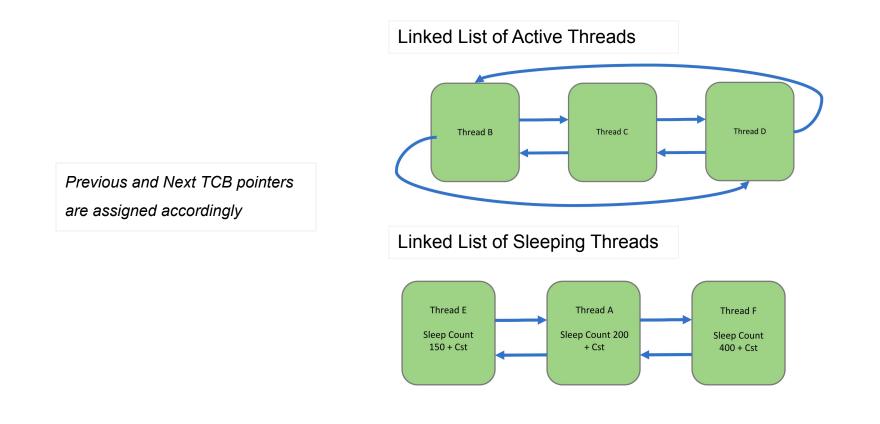


Since the list of active threads is Round-Robin (no priority), we can simply add it to the back of the linked list.













#### **Periodic Threads**

- A periodic thread is simply a function that performs a unique task after a certain amount of time has passed
- There are a few ways to trigger periodic threads:

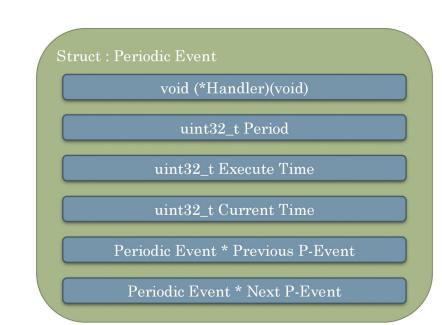
#### • Hardware Timer

- If the number of periodic tasks is small, we can allocate a unique hardware timer to each task.
- Alternatively, we could use just one timer, give each periodic thread a current time and period, and cycle through the events in round-robin fashion.
- SysTick Timer
  - We can use the scheduler as the timer to call periodic events before performing a context switch.
  - This is how it will be implemented for G8RTOS!





typedef struct ptcb\_t {
 void (\*handler)(void);
 uint32\_t period;
 uint32\_t executeTime;
 uint32\_t currentTime;
 struct ptcb\_t \*previousPTCB;
 struct ptcb\_t \*nextPTCB;
} ptcb\_t;



# Adding a Periodic Thread in a Linked List

```
/*
* Adds periodic threads to G8RTOS Scheduler
* Function will initialize a periodic event struct to represent event.
* The struct will be added to a linked list of periodic events
* Param Pthread To Add: void-void function for P thread handler
* Param period: period of P thread to add
* Returns: Error code for adding threads
*/
int G8RTOS_AddPeriodicEvent(void (*PthreadToAdd)(void), uint32_t period, uint32_t execution)
{
    // your code
}
```

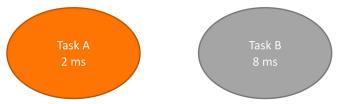
#### Recall your implementation of the G8RTOS\_AddThread function!



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# Periods with Common Multiples

• Suppose two periodic events exist with the following periods:



- **Task B** will always occur immediately **after** Task A, because its period is a multiple Task A's
- To combat this, we can give one P-Thread a difference initial **current time** other than 0
- Example:
  - Task A initial time = 0, Task B initial time = 1
  - Task A will run 3 times after 6 SysTick interrupts, and Task B will run on the 7th tick
- **Note:** In order for this system to work properly, the maximum time to execute each task must be very short compared to the period of the SysTick to avoid missing interrupts

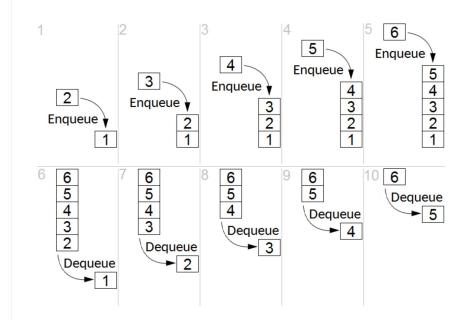
### Periodic vs Sleeping Threads

- Periodic threads are always in the active state
- Sleeping threads go between the active state and spinning in the sleep state
- Periodic threads ensure a periodic time more accurately
  - This is because when a thread is done sleeping, it doesn't necessarily mean it is currently **running**, but simply means it is **active** and able to run when it is its turn



### **Inter-Process Communication**

- We implement the **FIFO** structure as IPC
  - First In First Out
- FIFO
  - Maintain a linked list/array as queue
  - Write to the end of queue
  - Read from the begin of queue
- You have to implement
  - FIFO Initialization
  - Read from FIFO
  - Write to FIFO





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# Implementing FIFO

#### Hints

- No more than 4 FIFOs are needed in your G8RTOS
- You can static define FIFOs with array to improve performance
- Read/Write an int32\_t data from/into FIFO each time

#### Structures

- **Buffer where data will be held:** Int32\_t Buffer[FIFOSIZE]
- Head pointer: int32\_t \*Head
- Tail pointer: int32\_t \*Tail
- Lost data count: uint32\_t LostData
- Current Size semaphore: semaphore\_t CurrentSize
- Mutex semaphore: semaphore\_t Mutex

- typedef struct FIF0\_t {
  - int32\_t buffer[16];
  - int32\_t \*head;
  - int32\_t \*tail;
- uint32\_t lostData;
  - semaphore\_t currentSize;
    - semaphore\_t mutex;
- 29 } FIF0\_t;
- 30

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23

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- 31 /\* Array of FIFOS \*/
- 32 static FIF0\_t FIF0s[4];



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# **Read Function of FIFO**

- Parameter: an int32\_t value, which FIFO should be read
- Return value: an int32\_t value from the head of FIFO
- Mutex semaphore
  - Wait before reading from FIFO
  - In case the FIFO is being read from another thread

#### Current Size semaphore

- Wait before reading from FIFO
- In case the FIFO is empty
- When read is complete:
  - Update the head pointer
  - Signal the Mutex semaphore so other waiting threads can read

```
/*
 * Reads FIF0
 * - Waits until CurrentSize semaphore is greater than zero
 * - Gets data and increments the head pointer (wraps if necessary)
 * Param: "FIF0Choice": chooses which buffer we want to read from
 * Returns: uint32_t Data from FIF0
 */
int32_t readFIF0(uint32_t FIF0Choice)
{
    // your code
}
```





# Write Function of FIFO

#### • Parameter:

- An int32\_t value, which FIFO should be read
- An int32\_t value, data to be written into the tail of FIFO

#### Current Size semaphore

- The value should be compared with the FIFOSIZE-1.
  - Provides 1 buffer cell in case an interrupt happens between reading FIFO and incrementing its head.
  - If the value is larger than FIFOSIZE-1, increment the lost data value and overwrite the old data.
- Write the data
  - Signal the Current Size semaphore and notify other waiting threads the FIFO is not empty.

```
/*
 * Writes to FIF0
 * Writes data to Tail of the buffer if the buffer is not full
 * Increments tail (wraps if necessary)
 * Param "FIF0Choice": chooses which buffer we want to read from
 * "Data': Data being put into FIF0
 * Returns: error code for full buffer if unable to write
 */
int writeFIF0(uint32_t FIF0Choice, uint32_t Data)
{
 // your code
}
```



- 2 periodic threads and 5 background threads
- 3 FIFOs and 2 Semaphores
- BackGround Thread 0:
  - Empty default thread; does nothing (really?)
- BackGround Thread 1:
  - Read the BME280's temperature sensor
  - Sends data to temperature FIFO
  - Sleep for 500ms
- Periodic Thread 0 (Period: 100ms):
  - Read X-coordinate from the joystick
  - Write data to Joystick FIFO

6	<pre>#ifndef THREADS_H_</pre>
7	#define THREADS H
8	
9	<pre>#include "G8RTOS.h"</pre>
10	
11	<pre>#define J0YSTICKFIF0 0</pre>
12	#define TEMPFIF0 1
13	#define LIGHTFIF0 2
14	
15	<pre>semaphore_t *sensorMutex;</pre>
16	<pre>semaphore_t *LEDMutex;</pre>
17	
18	<pre>void BackGroundThread0(void);</pre>
19	<pre>void BackGroundThread1(void);</pre>
20	<pre>void BackGroundThread2(void);</pre>
21	<pre>void BackGroundThread3(void);</pre>
22	<pre>void BackGroundThread4(void);</pre>
23	
24	<pre>void Pthread0(void);</pre>
25	<pre>void Pthread1(void);</pre>
26	
27	<pre>#endif /* THREADS_H_ */</pre>





- 2 periodic threads and 5 background threads
- 3 FIFOs and 2 Semaphores
- Periodic Thread 1 (Period: 100ms):
  - Prints out the decayed average value of the joystick's X-coordinate in a UART console.
  - Prints out the temperature value in a UART console (in degrees Fahrenheit).
  - What if temp sensor does not work?
    - Use the gyro x-axis

6	<pre>#ifndef THREADS_H_</pre>
7	<pre>#define THREADS_H_</pre>
8	
9	<pre>#include "G8RTOS.h"</pre>
10	
11	<pre>#define JOYSTICKFIF0 0</pre>
12	#define TEMPFIF0 1
13	<pre>#define LIGHTFIF0 2</pre>
14	
15	<pre>semaphore_t *sensorMutex;</pre>
16	<pre>semaphore_t *LEDMutex;</pre>
17	
18	<pre>void BackGroundThread0(void);</pre>
19	<pre>void BackGroundThread1(void);</pre>
20	<pre>void BackGroundThread2(void);</pre>
21	<pre>void BackGroundThread3(void);</pre>
22	<pre>void BackGroundThread4(void);</pre>
23	
24	<pre>void Pthread0(void);</pre>
25	<pre>void Pthread1(void);</pre>
26	
27	<pre>#endif /* THREADS_H_ */</pre>



- 2 periodic threads and 5 background threads
- 3 FIFOs and 2 Semaphores
- BackGround Thread 2:
  - Read the light sensor.
  - Send data to light FIFO.
  - Sleep for 200ms.
- BackGround Thread 3:
  - Read temperature FIFO.
  - Output data to Red/Blue LEDs as shown in the figure.
     Feel free to adjust or normalize the temperature values if needed.

Temp > 84		
81 < Temp < 84		
78 < Temp < 81		
75 < Temp < 78		
72 < Temp < 75		
69 < Temp < 72		
66 < Temp < 69		
63 < Temp < 66		

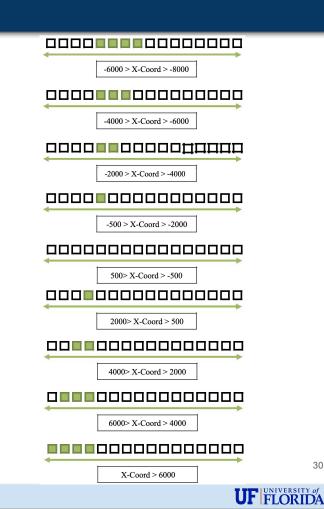


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- 2 periodic threads and 5 background threads
- 3 FIFOs and 2 Semaphores
- BackGround Thread 4:
  - Read the joystick's FIFO. 0
  - Calculate decayed average: to calculate a 50% decaying 0 average, you will have an int32 t variable (eg, named Avg). After getting a new value, Avg will be updated as

```
Ava =
     (Avg + value) >> 1
```

Output data to Green LEDs as shown in the figure.





#### Your Feedback Summary

#### ⇒ Positives

- Good contents, utility for career options, good hands-on and theory balance, good slides.
- The board setup is cool, manageable workload, templates are helpful.
- The bonus points and questions in-class are great. Topics are well structured.
- The book & slide contents are consistent with lab-work, RTOS seems manageable now.
- TAs are awesome and very helpful. They try to accommodate based on given situations.

#### ⇒ Things to improve:

- Some OS concepts should be covered at the beginning; crash-course was useful but late.
- Some Lab-2 solutions are in the book, should have more challenging problems.
- Should have more TAs, office hours are sparse.
- Lecture slides could be annotated. Lab solutions can be discussed.

#### ⇒ Other suggestions

- <u>Topics:</u> varied opinions about depth and time-spent (slow/fast) on certain topics.
- <u>Labs:</u> some theoretical/on-paper homeworks will be nice to complement the labs.
- <u>In-class:</u> Sometimes the class is too quiet (speaker is not turned on always).
- <u>Others:</u> ...

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Avg: 9.42 Median: 9.4

Min: 7 (# 5) Max: 12 (# 4)



### Logistics

 $\Rightarrow$  Lab-2 demo and quiz-1 ends this week

⇒ Lab-3 demos start from next Monday (Oct 10th)

#### ⇒ Mid Exam

- October 14th Friday: regular class time
- Time: 40 minutes
- We will discuss question patterns and practice questions in the coming weeks





